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NEGATIVE ACKNOWLEDGMENT (NAK) SUPPRESSION

Abstract of the Disclosure

Logic circuitry (409) constantly monitors a transmitter (410) to determine if the transmitter (410) is idle. Logic circuitry (409) suppresses all NAKs until data and other channel information is not being transmitted by the transmitter (410) and RF resources are not scarce. Additionally, logic circuitry (409) suppresses all NAKs until a predetermined number of NAKs has been buffered by the logic circuitry (409). More particularly, logic circuitry (409) determines when a number of NAKs will sufficiently fill an over-the-air frame. Once the predetermined number of NAKs has been collected, the logic circuitry (409) will generate the appropriate NAKs.